



શાંતિલાલ શાહ ઇજનેરી મહાવિદ્યાલય

(ગુજરાત રાજ્ય સંસ્થાન)

સિદસર કેમ્પસ, પોસ્ટ વર્તેજ, ભાવનગર - ૩૬૪ ૦૬૦

ગુજરાત, ભારત

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SSEC/SSIP/ 267

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Office Order:

In view of the strategy under New Industrial Policy, the Gujarat Government has introduced 'Scheme for Assistance for start-ups/ Innovation' vide its Resolution of Industries and Mines Department, No. MIS-102014-924909-I dated 27.01.2015. The Education Department (Higher and Technical), Government of Gujarat has published STUDENT STARTUP AND INNOVATION (SSIP) POLICY (2017-2021). A special focus under SSIP has been given on IPR protection and leveraging its impact.

The institute shall utilize the grant for students' start-ups for the following activities:

- Prototype support
- Patent support
- Activities, such as documentation, dissemination, creation of preincubation facility, conferences, hackathons, academic courses of start up and innovation, short-term training programmes, other pedagogical and academic interventions and other operational expenditure.

In this context, for the smooth functioning of SSIP activities at this institute, following Four SSIP Committees are framed. These committees are (1) Institute SSIP Committee (ISC) (2) IPR Scrutiny Committee and (3) Activity Wise Committee. (4) IPR Cell. All the members are informed to carry out various activities in accordance with the policy documents and other guidelines are available at <http://ssip.cteguj.in/> for ready reference.

Committee 2: Institute Level SSIP Committee

Sr. No.	Name of the Member	Qualification and current Engagements	Position in Committee
1.	Dr. M G Bhatt	Principal, SSEC Bhavnagar	Chairman
2.	Dr. K P Badgujar	Professor, EE Department, SSIP Coordinator	Member Secretary
3.	Mr. Chetan Kamdar	Industry Expert	Member
4.	Mr. Ketan Patel	Industry Expert/Innovator	Member
5.	Mr. Adhish Rokadia	CA, Finance Expert	Member
6.	Mr. Prashant Mamtara	Start-up echo System Expert	Member
7.	Dr. S. P. Bhatnagar	Academic Expert	Member
8.	Dr. S R Dwivedi	Academic Expert	Member
9.	Dr R K Jain	Professor, Civil Department, Academic Expert	Member
10.	Dr. A V Gohil	Professor, Prod. Department, Academic Expert	Member
11.	Prof. M V Bhatti	Asso. Prof., AM Dept. Academic Expert	Member
12.	Prof. S B Parmar	Asso. Prof, EC Dept Academic Expert	Member
13.	Prof. M R Jani	Asso. Prof. IC Dept, Academic Expert	Member

Committee 2: IPR Scrutiny Committee

Sr. No.	Name of the Member	Qualification and current engagements	Position in Committee
1.	Dr. M G Bhatt	Principal, SSEC Bhavnagar	Chairman
2.	Dr. K P Badgujar	Professor, (E), SSEC Bhavnagar, SSIP Coordinator	Member Secretary
3.	Mr. Shibaji Ghosh	Patent Expert, CSMCRI	Member
4.	Dr. Hamir Sapramar	BPTI, Bhavnagar	Member
5.	Mr. Ashish Pipaliya	PGVCL, Bhavnagar	Member
6.	Dr R K Jain	Professor, (C), SSEC Bhavnagar	Member
7.	Dr. K R Trivedi	Asso.Prof.,(EC), SSEC Bhavnagar	Member

Committee 3: Activity Wise SSIP Committee

Sr No.	Activity	Name of the Nodal Officer	Members
1.	SSIP Coordinator	Dr. K P Badgujar	Dr. U K Chhaya Prof. N P Soni Prof. K A Mehta
2.	SSIP Co-coordinators	Prof. B N Vaidya Prof. N P Soni	Prof. K A Patel Prof. A N Devani Prof. P H Lakhani Prof. T B Maniar Prof. D B Vaghela
3.	Institute Innovation and start up council /Pre-incubation process/ Co-working space/ Common facility/ Workshop/ Events/Conferences/Capacity Building/Training Program	Prof A A Pandya	Dr. M D Vadhvani Prof A J Mankad Prof R K Jani Prof T B Maniar
4	Awards/Recognition/Exposure/Technology platforms/ICT Portals/ Virtual incubation/ mentoring and allied support	Prof M P Patel	Prof S J Agrawat Prof A D Meniya Prof N D Jogadia
5	Setting up Common Innovation Center, Tinkering Lab, Fab Lab and/or Basic Prototyping Facility	Prof P S Ramanuj	Prof V B Pandya Prof N P Nirmal Prof D G Jani
6.	Proof of Concept (PoC) and Prototype Development Support (Procurement of Consumables/raw material/minor work)	Dr. K R Trivedi Prof. M A Jajal	Prof M J Vora Prof M P Jani Prof G V Dihora Prof V N Kalola Prof D H Solanki Prof B K Borisaniya
7.	Intellectual Property Right Support/ Patent Filing support	Dr. R K Jain Dr. K R Trivedi	Prof. M P Jani Prof. A M Upadhyay Prof. A D Kalani Prof. N S Sangani Prof. B J Chudasma Prof. K A Mehta
8.	Academic Interventions/ Course Development/Course Execution/Pedagogical Interventions / New Programs/ on Innovation Entrepreneurship / Special initiatives for SSIP /	Dr. U K Chhaya Prof A B Parmar	Dr. D N Vaishnav Prof K G Khatsuria Prof K D Chavda

	Documentation/Publication /Dissemination including reprography charges		
9.	Finance Committee	Prof B D Upadhyay	Prof N B Gohil
10	Departmental level SSIP committee (DSC) for Scrutiny of Innovation/Project/Research related Activities and sensitizes students to the get benefit of SSIP.	Head of the department Two senior faculty Mentor of the project/activity Student member (If any)	
11	Store cum Procurement: Purchase related to SSIP	Prof. Mehul Mehta HOD of the concerned dept. Mentor of project One faculty nominated by HOD of Dept.	Shri N A Jadeja Shri Ravi Pandya

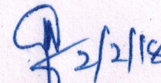
Committee 4: Institute IPR Cell

Sr. No.	Name of the Member	Qualification and current engagements	Position in Committee
1.	Dr. R K Jain	Professor, Civil Dept	Chairman
2.	Dr. K R Trivedi	Asso. Prof, EC. Department,	Member Secretary
3.	Prof M P Jani	Asso. Prof, IC. Department,	Member
4.	Prof. A M Upadhyay	Asso. Prof, EE. Department,	Member
5.	Prof. A D Kalani	Assi. Prof, Mech. Department,	Member
6.	Prof N S Sanghani	Assi. Prof, IT. Department,	Member
7.	Prof. B J Chudasma	Assi. Prof, Prod. Department,	Member
8.	Prof. K A Mehta	Assi. Prof, A.M. Department,	Member

Upon receiving an idea from students, the respective committee shall evaluate the idea to check whether the idea is feasible in the market or not. Once the idea is validated by the Department level SSIP committee (DSC) and Institute level SSIP committee (ISC), the students can avail financial support for proof of concept and prototype development. The students will also receive patent filing support if project/innovation is patentable approved by IPR Scrutiny committee.

The funds under the SSIP shall primarily be utilised for helping students producing a proof of concept, designing, filing patents and assist them on activities such as conducting workshops and programmes strictly in accordance with the financial guidelines provided under SSIP Project. The procedure for the receiving funds under SSIP is depicted in Appendix-1 with the flow chart.

All HODs must form department level SSIP committee for sensitization, scrutiny and echo system for smooth functioning and coordination at departmental level. Scrutinized proposals at the department level should be submitted to Institute level committee.


Principal

SSEC Bhavnagar

Principal

Shantilal Shah Engineering College,
Bhavnagar.

Copy to:

- (1) Dr. Mihir Shah, GKS, Gandhinagar
- (2) All the HODs
- (3) All concerned for information & necessary actions